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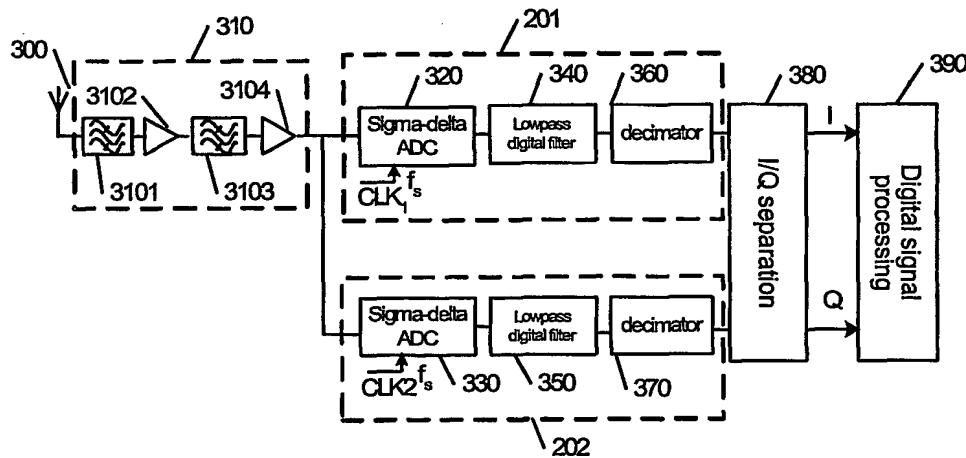
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(54) Title: NEW RECEIVER ARCHITECTURE FOR WIRELESS COMMUNICATION



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(57) Abstract: A bandpass-sampling receiver is proposed, comprising: the first Sigma-delta ADC, for converting the received RF signal into the first channel of digital signal under the control of the first sampling clock signal; the second Sigma-delta ADC, for converting the received RF signal into the second channel of digital signal under the control of the second sampling clock signal; a signal separating unit, for separating the in-phase signal and the quadrature signal in the first channel of digital signal and the second channel of digital signal. Low-resolution Sigma-delta ADC is adopted to sample and quantize the analog RF signal, thus the bandpass sampling receiver can save cost of ADCs.



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